

Fig.1

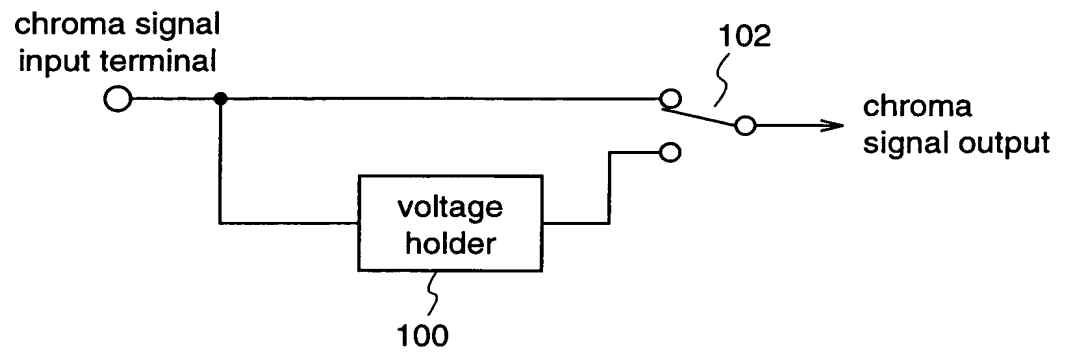


Fig.2

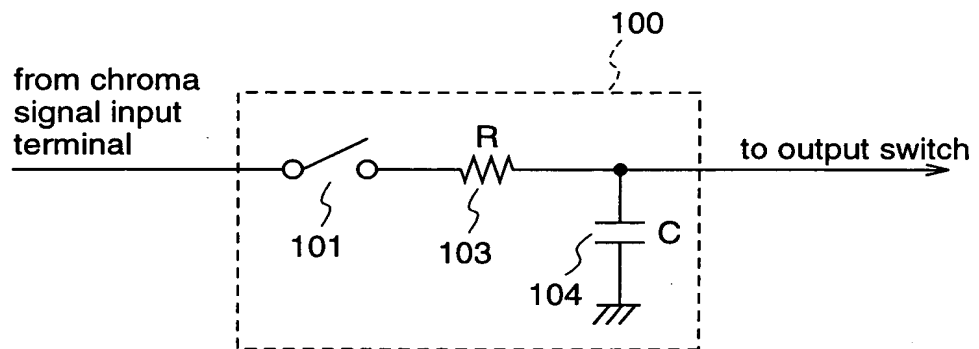


Fig.3 (a)
screen display

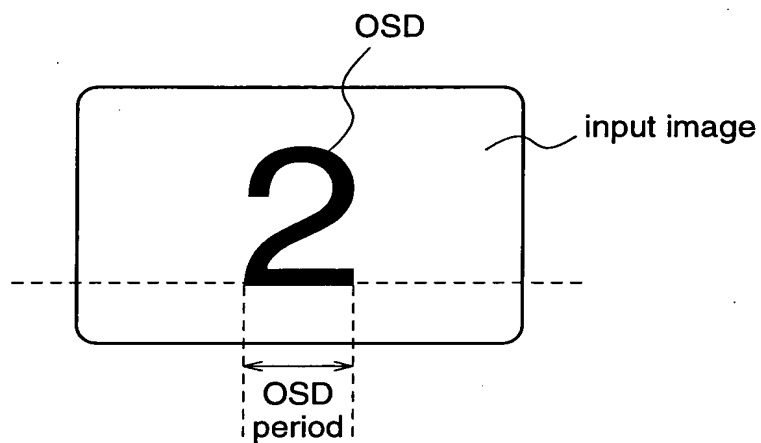


Fig.3 (b)
input luminance signal

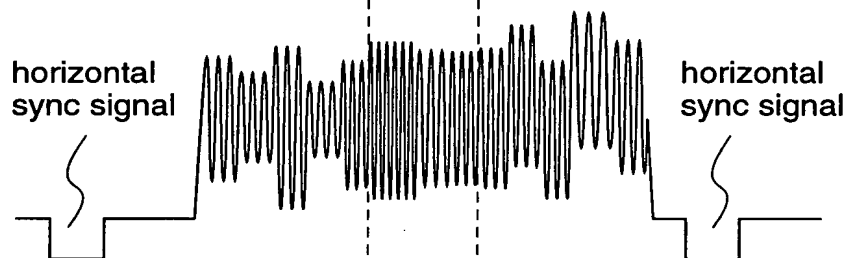


Fig.3 (c)
input chroma signal



Fig.3 (d)
output chroma signal

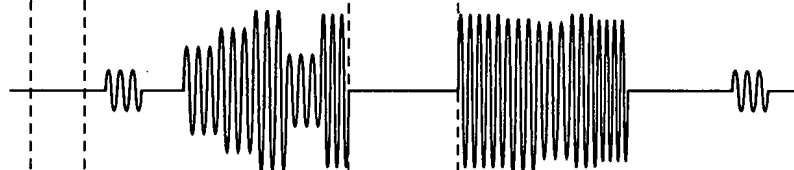


Fig.3 (e)
hold timing switch

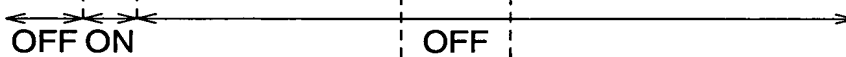


Fig.3 (f)
output switch

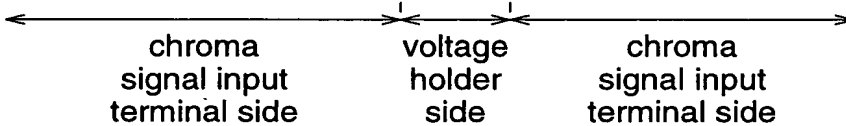


Fig.4

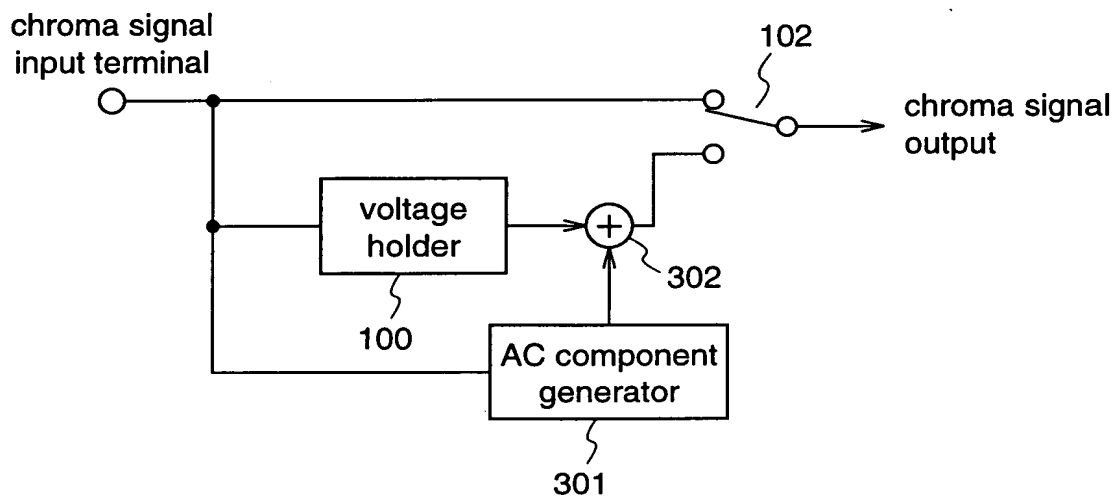


Fig.5

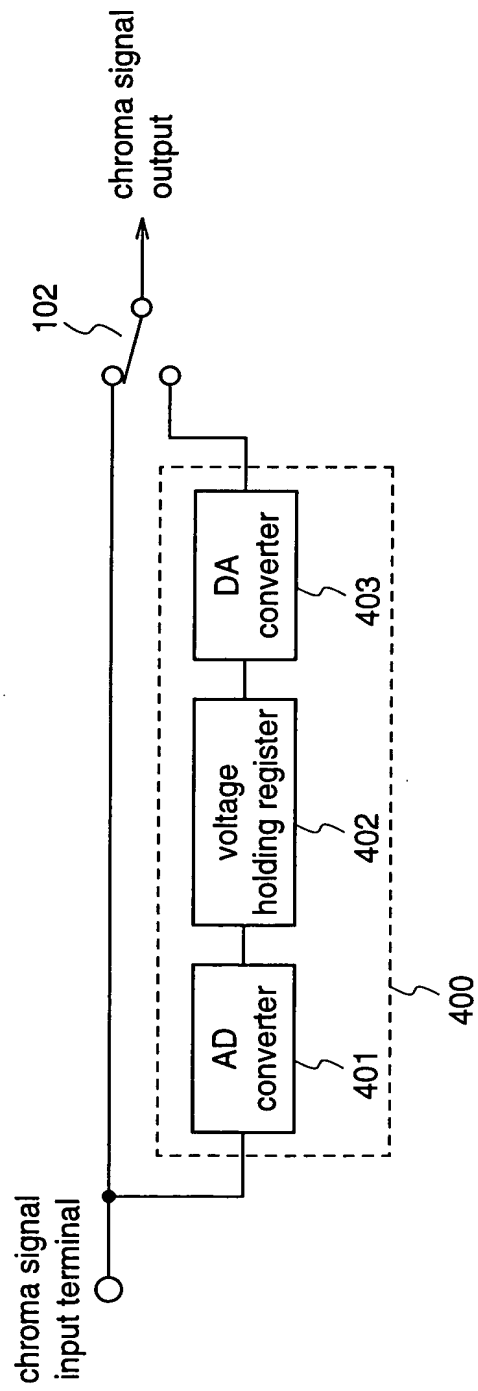


Fig.6

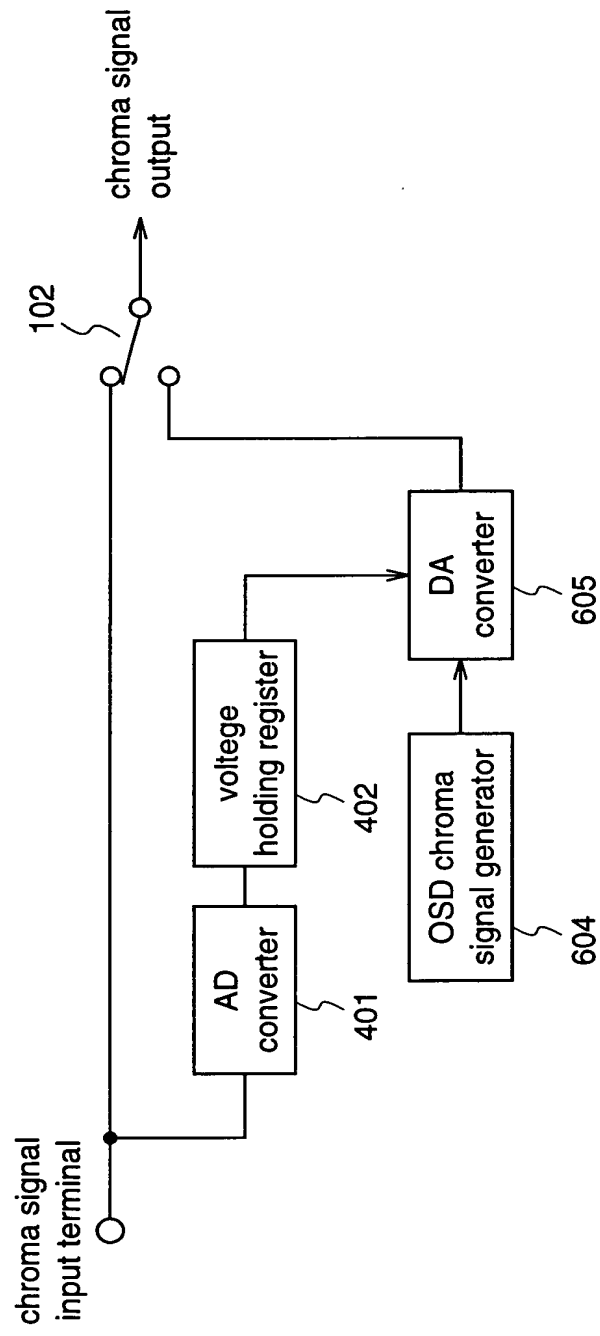
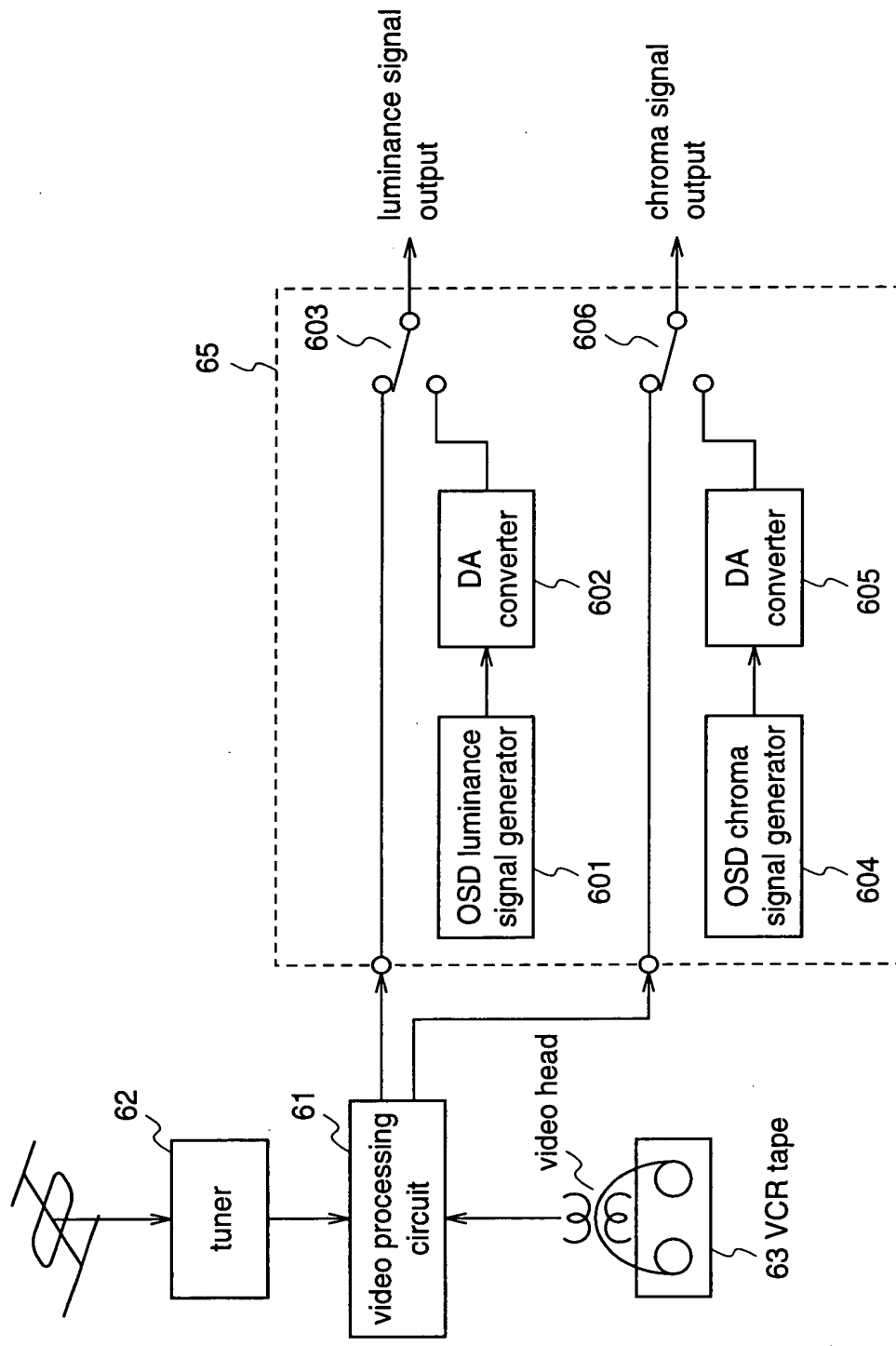


Fig.7 Prior Art



Prior Art

Fig.8 (a)
screen display

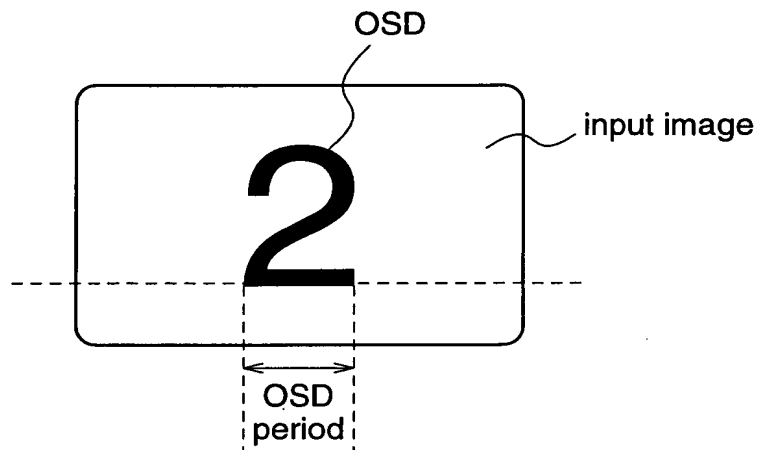


Fig.8 (b)
input luminance signal

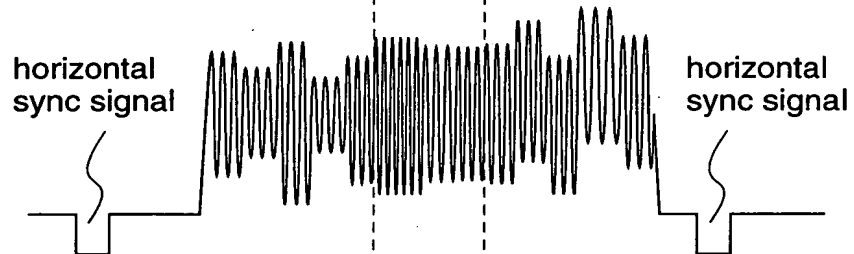


Fig.8 (c)
output luminance signal

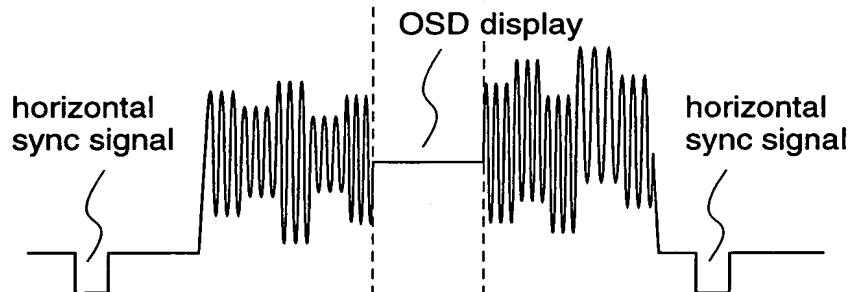
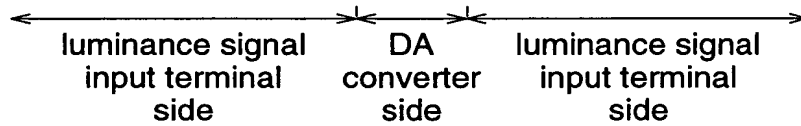


Fig.8 (d)
luminance signal output switch



Prior Art

Fig.9 (a)
screen display

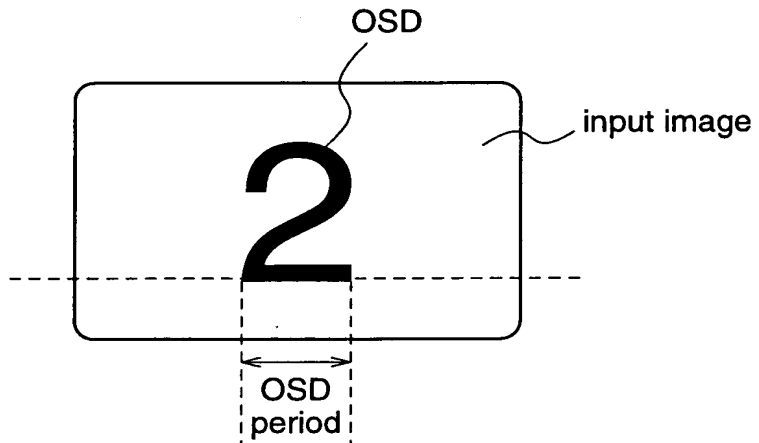


Fig.9 (b)
input chroma
signal

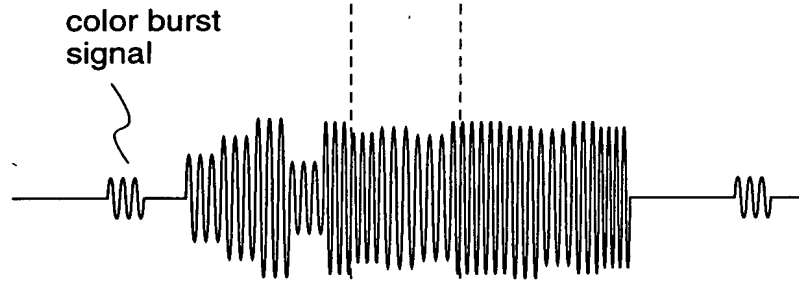


Fig.9 (c)
output chroma
signal

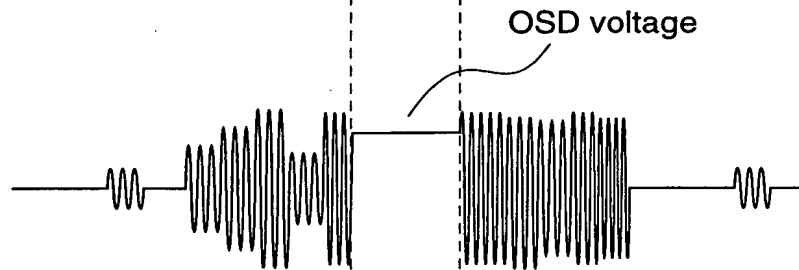


Fig.9 (d)
chroma signal
output switch

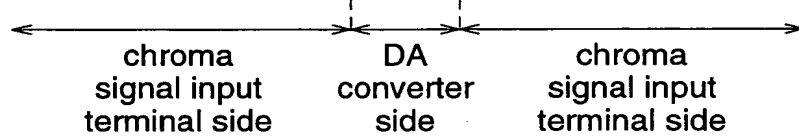


Fig.10 Prior Art

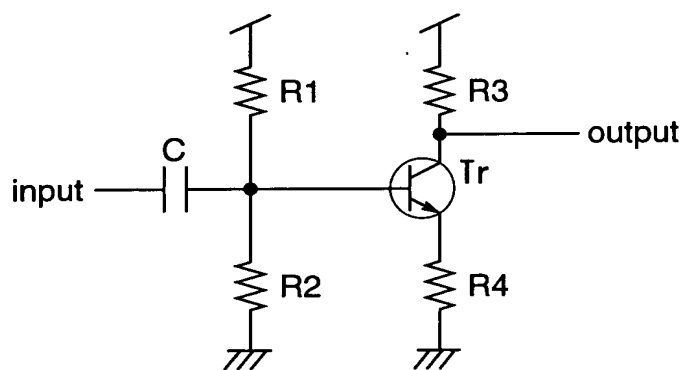


FIG. 10 PRIOR ART

Prior Art

Fig.11 (a)

input chroma signal

color burst signal

OSD output period

Fig.11 (b)

output chroma signal

VOB

0V

A

B

C

D

E

F

10022049 6422004

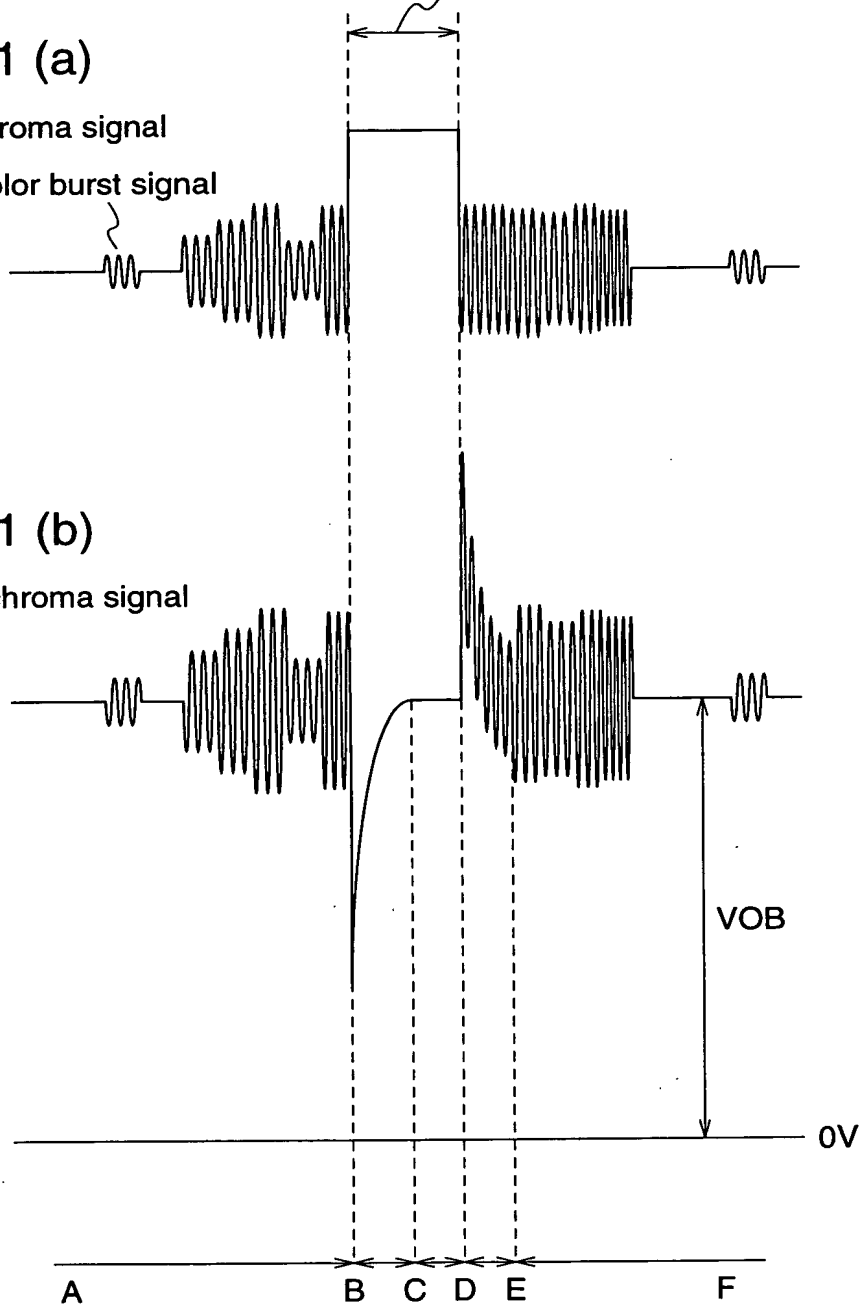
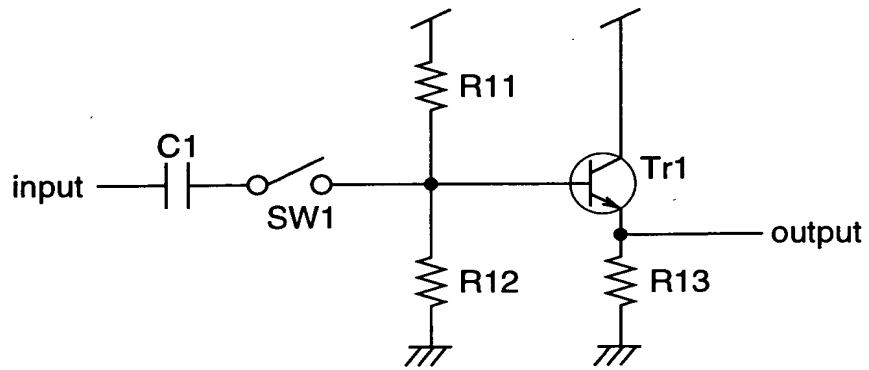


Fig.12 Prior Art



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